

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of: **Tokuharu KIMURA, et al.**

Serial Number: **Not Yet Assigned**

Filed: **January 27, 2004**

For: **COMPOUND SEMICONDUCTOR DEVICE AND ITS MANUFACTURE**

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

January 27, 2004

Sir:

In compliance with 37 CFR 1.56, Applicants call to the attention of the Patent and Trademark

Office the references listed on the attached PTO-1449.

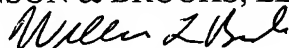
A copy of each of the references is enclosed herewith.

In the event there are any fees due in connection with the filing of this paper, please charge

Deposit Account No. 01-2340.

Respectfully submitted,

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PATENT TRADEMARK OFFICE

Enclosures: PTO-1449; References (3)

<b>INFORMATION DISCLOSURE STATEMENT PTO-1449</b>	Atty. Docket No. <b>040024</b>	Serial No. <b>New Application</b>
	Applicant(s): <b>Tokuharu KIMURA, et al.</b>	
	Filing Date: <b>January 27, 2004</b>	Group Art Unit: <b>Not Yet Assigned</b>

### U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
_____	AA					
_____	AB					
_____	AC					
_____	AD					

### FOREIGN PATENT DOCUMENTS

	Document No.	Date	Country	Translation (Yes or No)	
_____	AE	2001-230407	08/24/01	Japan	Yes-Abstract/Discussed in the specification
_____	AF	2002-016087	01/18/02	Japan	Yes-Abstract/Discussed in the specification
_____	AG	2002-359256	12/13/02	Japan	Yes-Abstract/Discussed in the specification
_____	AH				
_____	AI				

### OTHER DOCUMENTS

_____	AJ	
_____	AK	
Examiner		Date Considered